















- [15] R.A. Balda, J.H. Van Bommel, J.L. Willems, *Trends in Computer-Processes Electrocardiograms*. 1977.
- [16] M.L. Ahlstrom, W.J. Tompkins, *Automated High Speed Analysis of Holter Types with Microcomputers*, Biomedical Engineering, IEEE Transaction on, vol. BME-30, pp 651-657, 1983.
- [17] W.A.H. Engelse, C. Zeeleberg, *A simple scan algorithm for QRS detection and feature extraction*, IEEE Computer Society, pp 37-42, 1979.
- [18] M. Okada, *A Digital Filter for the QRS Complex Detection*, Biomedical Engineering, IEEE Transaction on, vol. BME-26, pp 700-703, 1984.
- [19] J. Pan and W.J. Tompkins, *A real-time QRS detection algorithm*, IEEE Trans. Biomed. Eng., vol. BME-32, pp. 230-236, 1985.
- [20] Massachusetts Institute of Technology, *The Apnea-ECG Database*. Available: <https://www.physionet.org/physiobank/database/apnea-ecg/>.
- [21] ANSI/AAMI EC57: Testing and reporting performance results of cardiac rhythm and ST segment measurement algorithms (AAMI Recommended Practice/American National Standard, 1988. Available: <http://www.aami.org>.
- [22] F. Silvestri, S. Acciarito, G.C. Cardarilli, G.M. Khanal, L. Di Nunzio, R. Fazzolari, M. Re, *FPGA Implementation of a Low-power QRS extractor*, in Lecture Notes in Electrical Engineering, 2018 (Article In Press).
- [23] Cardarilli, G.C., Di Nunzio, L., Re, M. *Arithmetic/logic blocks for fine-grained reconfigurable units* (2009) Proceedings - IEEE International Symposium on Circuits and Systems, art. no. 5118184, pp. 2001-2004
- [24] Cardarilli, G.C., Di Nunzio, L., Fazzolari, R., Re, M., Lee, R.B., *Butterfly and inverse butterfly nets integration on Altera NIOS-II embedded processor*, Conference Record - Asilomar Conference on Signals, Systems and Computers, art. no. 5757737, pp. 1279-1283.
- [25] Cardarilli, G.C., Di Nunzio, L., Fazzolari, R., Pontarelli, S., Re, M., Salsano, A., *Implementation of the AES algorithm using a Reconfigurable Functional Unit*, ISSCS 2011 - International Symposium on Signals, Circuits and Systems, Proceedings, art. no. 5978668, pp. 97-100.
- [26] Cardarilli, G.C., Di Nunzio, L., Fazzolari, R., Re, M., *Fine-grain reconfigurable functional unit for embedded processors*, Conference Record - Asilomar Conference on Signals, Systems and Computers, art. no. 6190048, pp. 488-492.
- [27] Cardarilli, G.C., Di Nunzio, L., Fazzolari, R., Pontarelli, S., Re, M., *A reconfigurable functional unit for modular operations*, Lecture Notes in Electrical Engineering, 289, pp. 141-152.
- [28] Cardarilli, G.C., Di Nunzio, L., Fazzolari, R., Re, M., *TDES cryptography algorithm acceleration using a reconfigurable functional unit*, 21st IEEE International Conference on Electronics, Circuits and Systems, ICECS 2014, art. no. 7050011, pp. 419-422.